**Weekly Report Project (Week 8 – 11/14)**

1. **Last week:**

* Submitted schematic and layout
* Corrected error follow feedback from Professor Greenberg
* Already order the boards through PCB Unlimited
* Initialized all the pins on the microcontroller

1. **Next week:**

* Start to solder components on the boar
* Verify code functions

1. **Issue:**

* Unable programing to control clock signals